

Figure 1

Memory Module 250 DRAM 253 DRAM 256 DRAM 254 DRAM 255 Data Cache and Logic 258 Buffer 252 265 Memory Module 240 DRAM 243 DRAM 246 DRAM 244 DRAM 245 Data Cache and Logic 248 Buffer 242 System Memory 210 265 Memory Module 230 DRAM 233 DRAM 236 DRAM 234 DRAM 235 Data Cache and Logic 238 Buffer 232 265 Memory Module 220 DRAM 223 DRAM 224 DRAM 226 DRAM 225 Data Cache and Logic 500 Buffer 222 265 Memory Controller 202 Cache Tag and Logic 300

TOTEST. SEZOT

Figure 2

ronsessant action

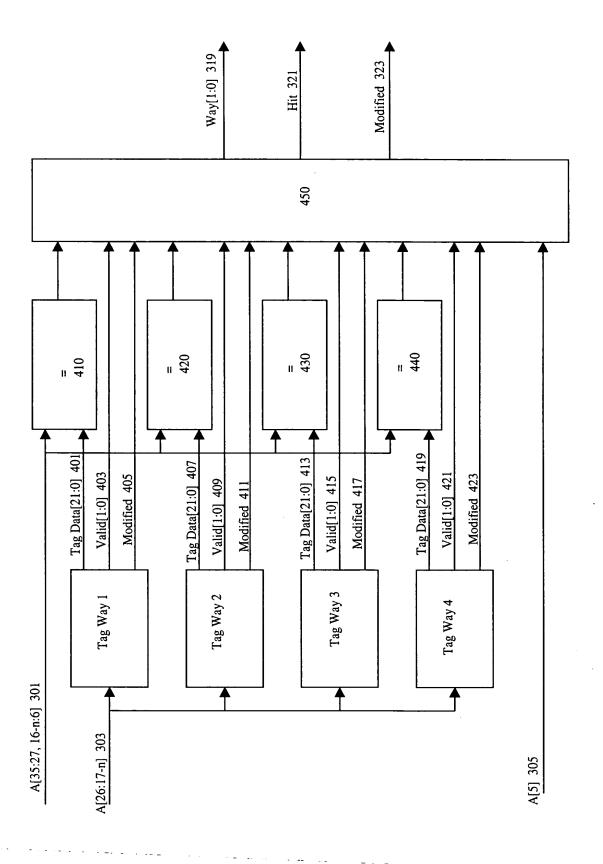


Figure 4

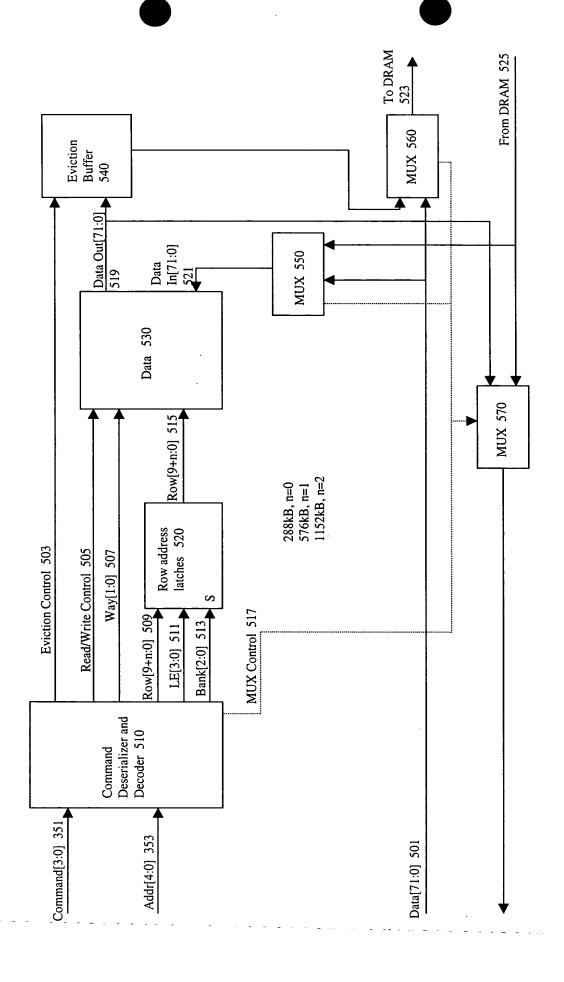


Figure 5

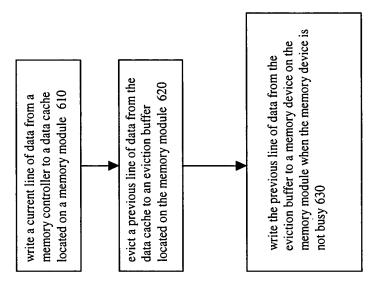


Figure 6